

11017 U.S. PTO
09/945500



365	185.28
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent

O.I.P.E.	PATENT DATE
SCANNED <i>MT</i> <i>104</i> Q.A. <i>104</i>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/945500		365	185.28	2818	Pham

APPLICANTS

Leonard Forbes

TITLE

Programmable memory address and decode circuits with low tunnel barrier interpoly insulators

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
365	185.28	365	185.05	185.06	185.26			
INTERNATIONAL CLASSIFICATION		257	17	9	135	220	802	
G11C	10/07	438	156	157	175	176		206
H01L	29/06		212		263		268	270
H01L	21/335		282					
H01L	21/338							
H01L	21/336							

☐ Continued on Issue Slip Inside File Jacket

Formal Drawings (3/23/99)

<input checked="" type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. <i>17</i> Figs. Drwg. <i>12</i> Print Fig. <i>50</i>			CLAIMS ALLOWED Total Claims <i>31</i> Print Claim for O.G. <i>1</i>	
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	ISSUE FEE Amount Due <i>1630</i> Date Paid _____				
ISSUE BATCH NUMBER					

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FORMAL DRAWINGS

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